AMENDMENTS TO THE CLAIMS

Claims 1-11. (Canceled).

- 12. (Previously Presented) An electric device comprising: a first elongated nanowire on an insulating surface and a second elongated nanowire on said insulating surface at a right angle to said first elongated nanowire and separated therefrom by a gap of between 0.4 nm and 10 nm.
- 13. (Original) The electric device of Claim 12 wherein said first and second nanowires form a transistor having a source, drain, and gate, and wherein said first nanowire has first and second ends; said first end of said first nanowire forming said source, said second end of said first nanowire forming said drain, and said second nanowire forming said gate.
- 14. (Previously Presented) The electric device of Claim 12 wherein said first elongated nanowire comprises a semiconductor chosen from the group consisting of Si, Ge, Ge_xSi_{1-x} where 0<x<1, GaAs, InAs, AlGaAs, InGaAs, InGaAs, GaN, InN, AlN, AlGaN, and InGaN.
- 15. (Previously Presented) The electric device of Claim 12 wherein said gap is filled with a material that stores electrical charge.
- 16. (Previously Presented) The electric device of Claim 12 wherein said gap is filled with a material having electric dipole moment.

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- 17. (Previously Presented) The electric device of Claim 12 wherein said first and second nanowires form a two-electrode memory switching device, said first nanowire forming the first electrode of said switching device and said second nanowire forming the second electrode of said switching device.
- 18. (New) An electric device comprising: a first elongated nanowire on and touching an insulating surface and a second elongated nanowire on and touching the same side of said insulating surface at a right angle to said first elongated nanowire, said first and second nanowire separated by a gap of between 0.4 nm and 10 nm.
- 19. (New) The electric device of Claim 18 wherein said first and second nanowires form a transistor having a source, drain, and gate, and wherein said first nanowire has first and second ends; said first end of said first nanowire forming said source, said second end of said first nanowire forming said drain, and said second nanowire forming said gate.
- 20. (New) The electric device of Claim 18 wherein said first elongated nanowire comprises a semiconductor chosen from the group consisting of Si, Ge, Ge_xSi_{1-x} where 0<x<1, GaAs, InAs, AlGaAs, InGaAs, GaN, InN, AlN, AlGaN, and InGaN.
- 21. (New) The electric device of Claim 18 wherein said gap is filled with a material that stores electrical charge.
- 22. (New) The electric device of Claim 18 wherein said gap is filled with a material having electric dipole moment.

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23. '(New) The electric device of Claim 18 wherein said first and second nanowires form a two-electrode memory switching device, said first nanowire forming the first electrode of said switching device and said second nanowire forming the second electrode of said switching device.

24. (New) An electric device comprising: a first elongated nanowire on an insulating surface and a second elongated nanowire on said insulating surface at a right angle to and in the same plane as said first elongated nanowire, said first and second nanowire separated by a gap of between 0.4 nm and 10 nm.

25. (New) The electric device of Claim 24 wherein said first and second nanowires form a transistor having a source, drain, and gate, and wherein said first nanowire has first and second ends; said first end of said first nanowire forming said source, said second end of said first nanowire forming said drain, and said second nanowire forming said gate.

- 26. (New) The electric device of Claim 24 wherein said first elongated nanowire comprises a semiconductor chosen from the group consisting of Si, Ge, Ge_xSi_{1-x} where 0<x<1, GaAs, InAs, AlGaAs, InGaAs, GaN, InN, AlN, AlGaN, and InGaN.
- 27. (New) The electric device of Claim 24 wherein said gap is filled with a material that stores electrical charge.
- 28. (New) The electric device of Claim 24 wherein said gap is filled with a material having electric dipole moment.

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(New) The electric device of Claim 24 wherein said first and second nanowires form a two-electrode memory switching device, said first nanowire forming the first electrode of said switching device and said second nanowire forming the second electrode of said switching device.

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